

## **DESIGN SYSTEM**

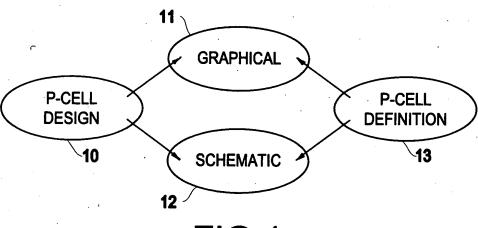


FIG.1

## **DESIGN SYSTEM**

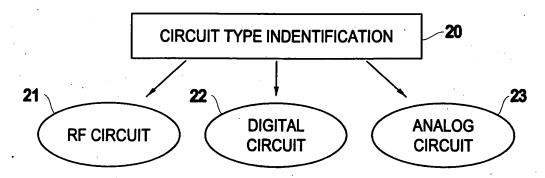


FIG.2



## **DESIGN SYSTEM**

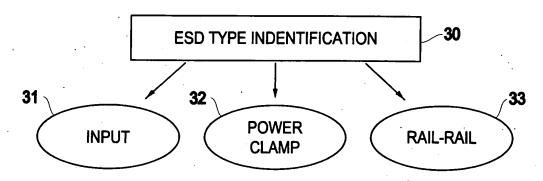


FIG.3

## **DESIGN SYSTEM**

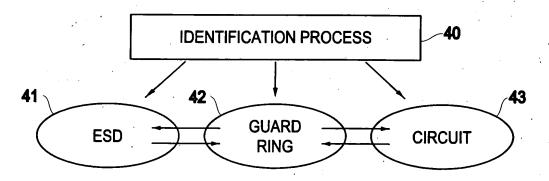
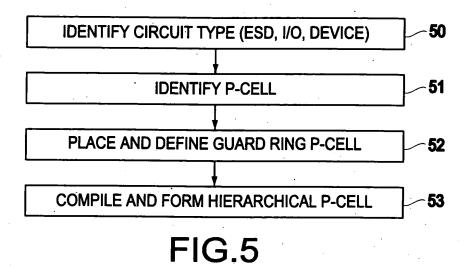


FIG.4



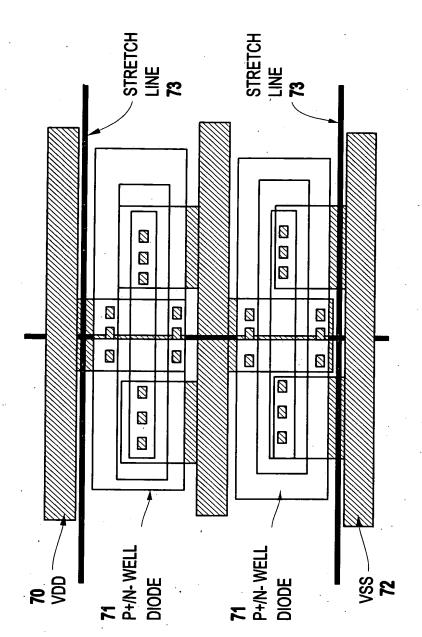


□ P-CELL
☐ GUARD RING P-CELL
☐ FILL OPTIMIZATION
□DT
☐ NW RING
□TI

P-CELL/GUARD RING DESIGN P-CELL GRAPHIC UNIT INTERFACE

FIG.6

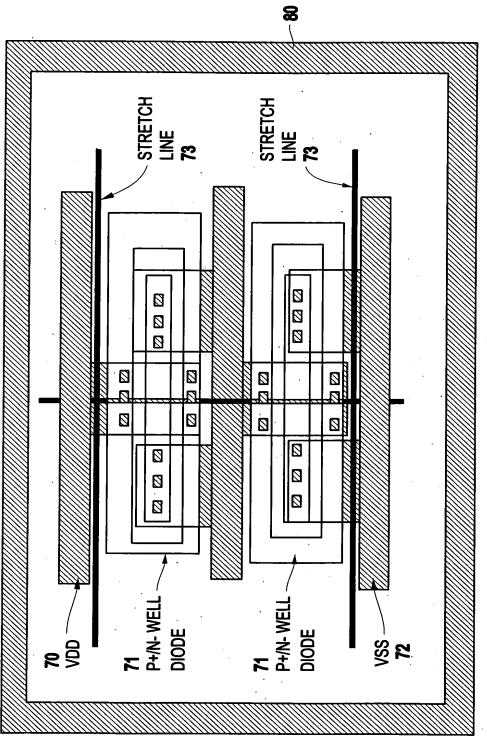




GRAPHICAL ESD HIERARCHICAL P-CELL

FIG.7

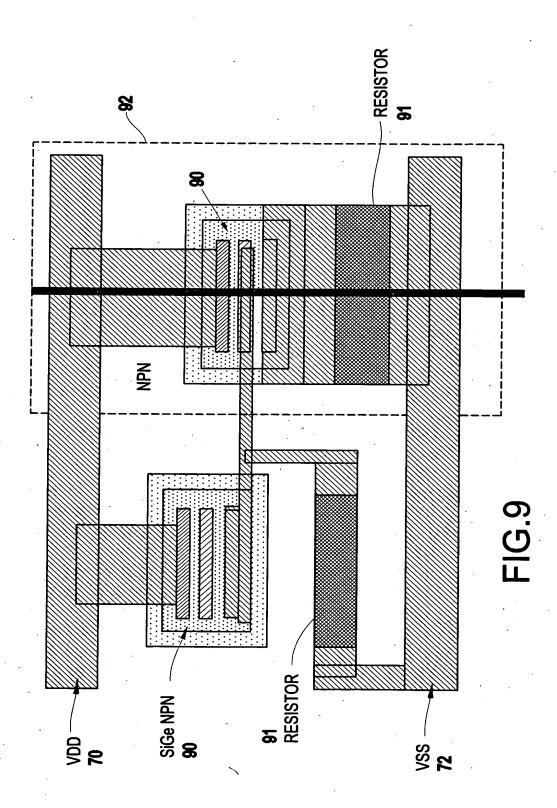




ESD HIERARCHICAL P-CELL WITH GUARD RING

FIG.8







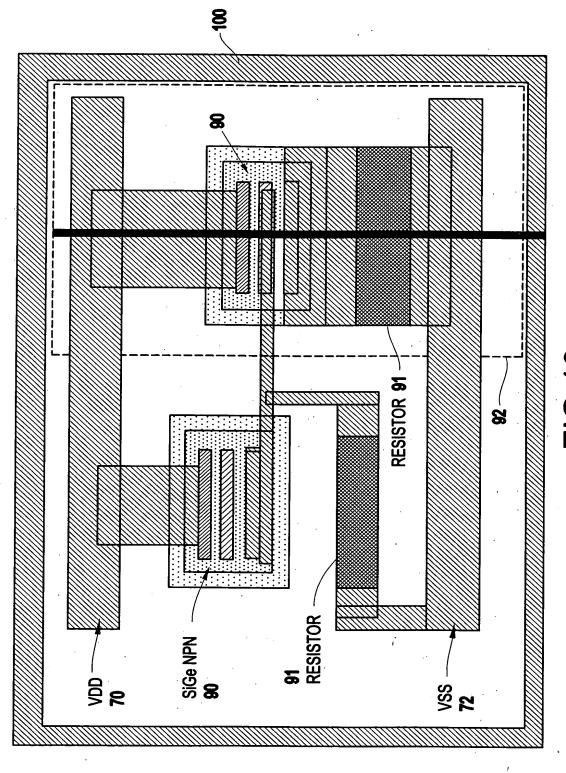
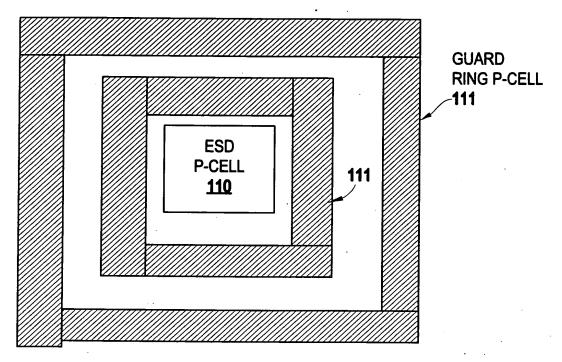


FIG.10





**FIG.11** 

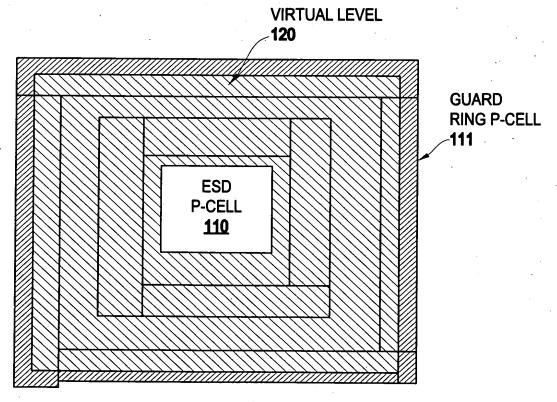
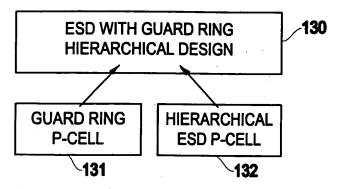


FIG.12





**FIG.13** 

